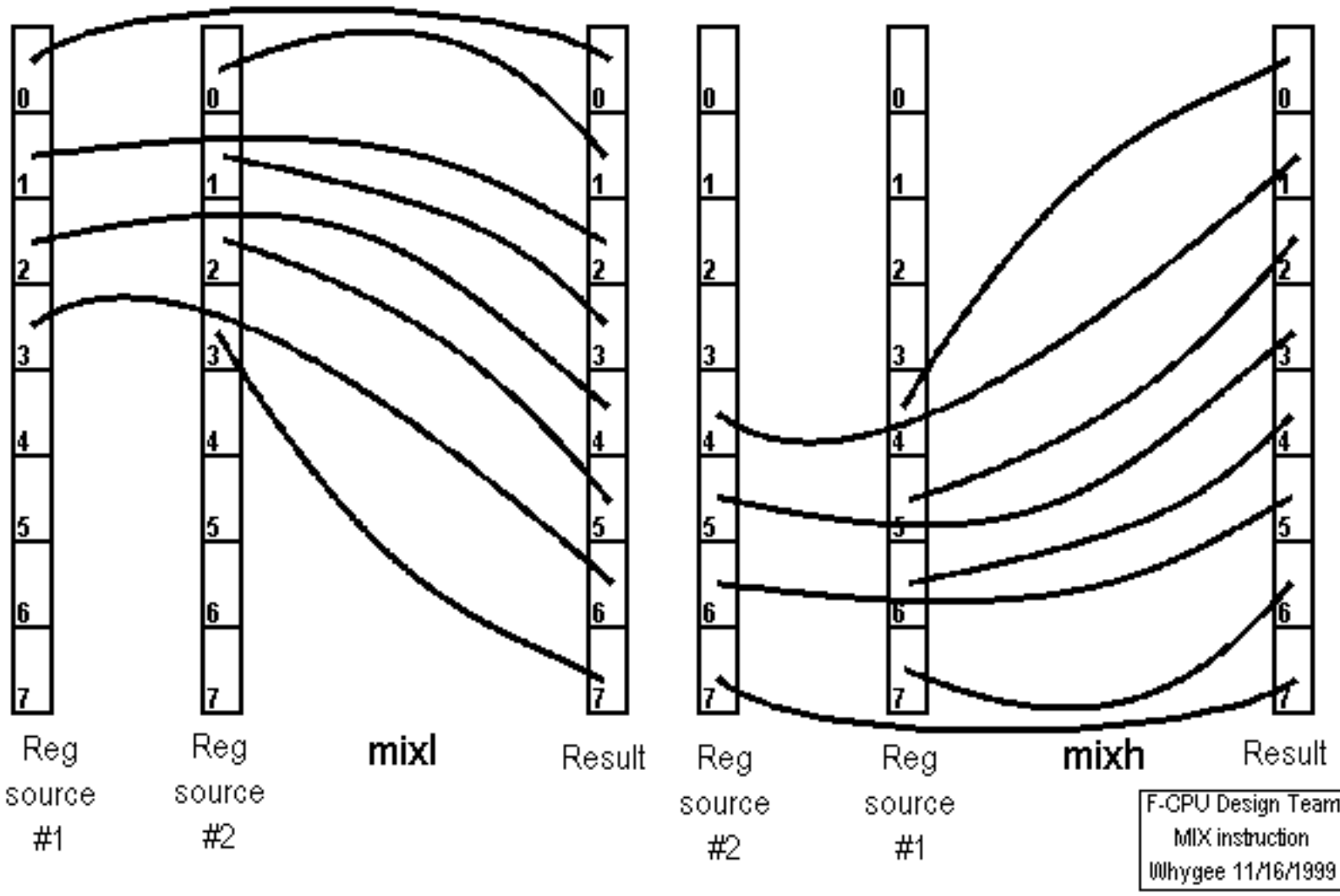
0
1
2
3
4
5
6
7
Reg source #1
Reg source #2
mixl
Result
Reg source #2
Reg source #1
mixh
Result
F-CPU Design Team
MIX instruction
Whygee 11/16/1999